

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yamauchi, K.

Serial No.: 09/733,036

Filed: December 11, 2000

For: SEMICONDUCTOR LASER DIODE CHIP AND ITS POSITIONING AND MOUNTING METHOD

Honorable Assistant Commissioner of Patents  
Washington, D.C. 20231

Group Art Unit: 2828

Examiner: Leung, Q.



#12  
Bel  
H7er

RECEIVED  
NOV -6 2002  
TC 2800 MAIL ROOM

**PETITION AND FEE FOR EXTENSION OF TIME**  
**UNDER 37 C.F.R. 1.136(a)**

Sir:

Applicant hereby petitions for a One-month extension of time to respond to the Office Action mailed July 2, 2002. A check for the extension fee in the amount of \$110.00 is attached. A Request for Continued Examination (RCE) is being filed simultaneously herewith. A duplicate copy of this sheet is enclosed.

The Commissioner is authorized to charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

11/05/2002 CNBUYEN 00000045 09733036

02 FC:1251

110.00 00

Respectfully submitted,

Sean M. McGinn, Esq.  
Reg. No. 34,386

Date:

11/4/02  
McGinn & Gibb, PLLC

8321 Old Courthouse Rd. Suite 200

Vienna, VA 22182-3817

(703) 761-4100

Customer No. 21254